

1046 U.S. PTO
10/001992
02/21/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10081992	02/21/2002	438		2812	POMPEY, R

**APPLICANTS: Jin Been-Yih; Arghavani Reza; Chau Robert;

RP Yes

**CONTINUING DATA VERIFIED:

RP None

** FOREIGN APPLICATIONS VERIFIED:

RP None

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO 10559-587001 / P12768
TITLE : Non-silicon semiconductor and high-k gate dielectric metal oxide semiconductor field effect transistors			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94) 41			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
		Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH: DISK (CRF) CD-ROM
(Attached in pocket on right inside flap)